



DLP-uCF917

PIC16F917 TARGET BOARD

1.0 INTRODUCTION

The DLP-uCF917 Target Board is designed to easily connect to the DLP-FLASH2 Device Programmer/Debugger and provide the design engineer with a low-cost hardware platform for developing and testing applications for the PIC16F917 microcontroller.

(Refer to the schematic at the end of this datasheet for additional details.)

2.0 SPECIFICATIONS

Program Memory: 8K x 14

2.0-5.5V Operation**

• RAM Size: 352 x 8

• 31 I/O (8x10b Analog)

• EEPROM Size: 256 x 8

LED Indicator

• Size: 1.92 x 0.89 Inches

**Note: If this target board is operated at a voltage other than 5.0 volts via a user-supplied power source, then the TPWR jumper must be removed on the DLP-FLASH2 Programmer.

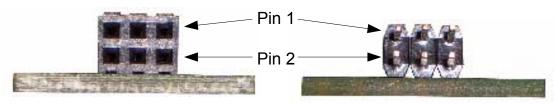
3.0 OSCILLATOR OPTIONS

The PIC16F917 has an internal, factory-calibrated (1%) oscillator with eight user-selectable frequencies from 31 kHz to 8 MHz. Refer to the datasheet for the PIC16F917 for additional details.

A user-supplied crystal and associated capacitors can also be added if a specific operating frequency is required.

4.0 PROGRAMMER/DEBUGGER INTERFACE

The DLP-FLASH2 Programmer/Debugger utilizes a 6-pin, 2mm female header for connection to the target device. The following shows the pinout for the programming interface connectors:



DLP-FLASH2

Target Header

Pin#	Description	Alternate Name
1	PGM	B3
2	PGC	B6
3	PGD	B7
4	Ground	
5	Target Power	TVDD
6	MCLR	Vpp

5.0 DISCLAIMER

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This document provides preliminary information that may be subject to change without notice.

6.0 CONTACT INFORMATION

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PGC/B6 TARGET BOARD - 16F917 DLP-uCF917 RB1/SEG1
RB2/SEG2
RB3/SEG3
RB4/COM0
RB5/COM1
RB6/PGC/ICDCK/SEG14
RB7/PGD/ICDDAT/SEG13 RE0/AN5/SEG21 RE1/AN6/SEG22 RE2/AN7/SEG23 RC0/VLCD1 RC1/VLCD2 RC2/VLCD3 RC3/SEG8 RC5/T1CKI/CCP1/SEG10 RC6/TX/CK/SCK/SCL/SEG9 RC7/RX/DT/SDI/SDA/SEG8 RB0/INT RC4/T1G/SDO/SEG11 Design 2 2 __13 __12 __34 __33 NC NC NC RA3/AN3/VREF+/C1+/SEG15 RA4/TOCKI/C1OUT/SEG4 RA5/AN4/SS/C2OUT/SEG5 C5 : 0.1uF RA0/AN0/C1-/SEG12 RA1/AN1/C2-/SEG7 RA2/AN2/VREF-/C2+/COM2 RA7/OSC1/CLKI/T1OSI RA6/OSC2/CLKO/T1OSO VDD VDD 1 6 RE3/MCLR/Vpp Vss RD2/CCP2 RD3/SEG16 RD4/SEG17 RD5/SEG18 RD6/SEG19 RD7/SEG10 Vss RD0/COM3 PIC16F917 √TVDD RD1 18 A5 84884 4 Z ⟨Vpp/MCLR ≪TVDD 18pF ပ္သ 18pF 2 Vpp/MCLR> PGC/B6} ᆒ 160 R2 ᆒ J2 HEADER 18X2 2x3 Programming Header \Box **GRN LED** ᆒ 人TVDD .- 0 **≪TVDD** В С